



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

(AUTONOMOUS)

Accredited by NAAC & NBA (CSE, IT, ECE, EEE & ME)

Approved by AICTE, New Delhi and Affiliated to JNTUK, Kakinada

L.B.Reddy Nagar, Mylavaram-521230, Krishna Dist, Andhra Pradesh, India

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Report on

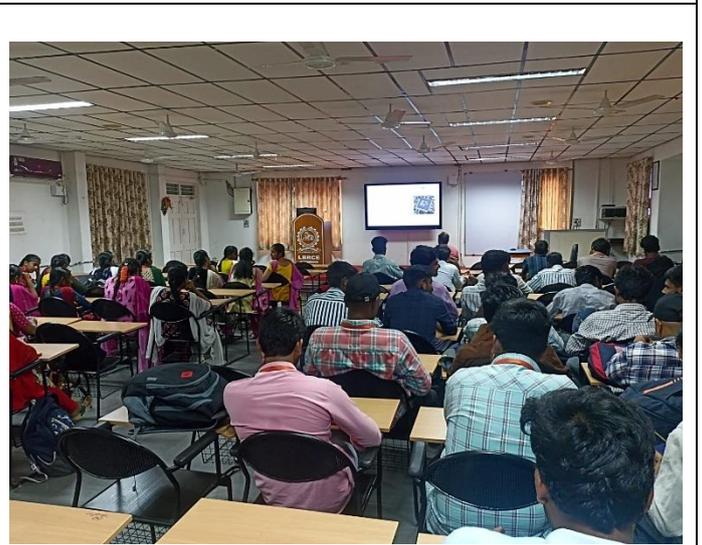
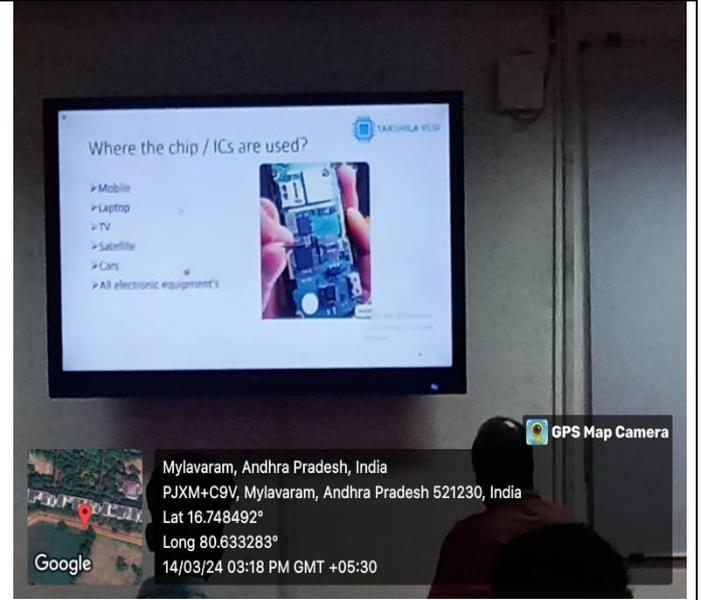
Job opportunities in VLSI / Semiconductor Industry

Event Type	:	Guest Lecture
Date / Duration	:	14-03-2024, 3.00PM to 4.30 PM
Resource Person	:	Arun, Physical Design Engineer, 10+ year experience, Takshila Institute of VLSI Technologies
Names of Coordinators	:	Mr. Y. Raghuvamsi
Target Audience	:	All 3 rd year Students of EEE, LBRCE
Total no of Participants	:	121
Objective of the event	:	To create awareness about the job opportunities in VLSI/ Semiconductor Industry.
Outcome of event	:	<ul style="list-style-type: none">➤ Students have acquired the information on VLSI importance, and the present scenario of VLSI in industries.➤ Understand about the placement opportunities for the concepts of VLSI.➤ Understand about the technical tools required for VLSI.

Description / Report on Event:

The EEE Department is conducted an online webinar on “**Job opportunities in VLSI/ Semiconductor industry**” on March 14th, 2024. This webinar has covered the concepts of physical design, analog circuits design and their layout design, ASIC verification and the testing design.

Photographs :



Coordinators:
Mr. Y. Raghuvamsi, Sr. Asst. Professor

HOD/EEE